

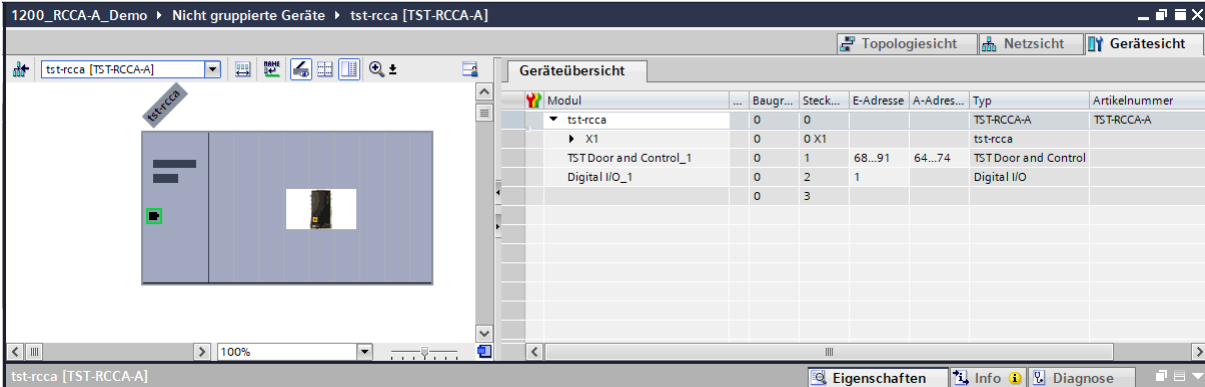
Interpretation of the communication data using the example of an S7 1200

Data Structure

Offset

Byte	bit 7							bit 0
0	Door Position							
1	Door Status							
2	Door Operation mode							
3	Cycle Count							
4								
5								
6								
Status bits								
7	door closing	door opening	RESERVED	Safety edge triggered	Photo eye triggered	Service pending	Service warning	automatic possible
8	not used	not used	not used	not used	log event available	tst in para mode	door in closed pos	door in open pos
9	Maintenance Counter							
10								
11								
12								
TST Input status								
13	SLE_2	SLI_2	SLE_1	SLI_1	DET_4	DET_3	DET_2	DET_1
14	Foil_close	Foil_stop	Foil_open	E-Stop_Ex2	E-Stop_Ex1	E-Stop_INT	RADIO_2	RADIO_1
15	Input_8	Input_7	Input_6	Input_5	Input_4	Input_3	Input_2	Input_1
16	Input_31	Input_15	Input_14	Input_13	Input_12	Input_11	Input_10	Input_9
17	Input_28	Input_27	Input_26	Input_25	Input_24	Input_23	Input_22	Input_21
18	not used	not used	Input_3F	Input_3E	Input_3D	Input_3C	Input_3B	Input_3A
TST Output status								
19	Output_8	Output_7	Output_6	Output_5	Output_4	Output_3	Output_2	Output_1
20	not used	Output_15	Output_14	Output_13	Output_12	Output_11	Output_10	Output_9
21	Output_24	Output_23	Output_22	Output_21	RESERVED	Output_2F	Output_2E	Output_2D
22	Output_2C	Output_2B	Output_2A	Output_29	Output_28	Output_27	Output_26	Output_25
23	Output_38	Output_37	Output_36	Output_35	Output_34	Output_33	Output_32	Output_31

Hardware I/O section



Modul	Baugr...	Steck...	E-Adresse	A-Adres...	Typ	Artikelnummer
tst-rcca	0	0			TST-RCCA-A	TST-RCCA-A
X1	0	0 X1			tst-rcca	
TST Door and Control_1	0	1	68...91	64...74	TST Door and Control	
Digital I/O_1	0	2	1		Digital I/O	
	0	3				

Direct access in control programme

Modul	Ba...	Ste...	E-Adresse	Adresse	Anzeige...	Beobachtungswert	Kommentar
tst-rcca	0	0		1 %B68	Hex	16#20	Door position
X1	0	0 X1		2 %B69	Hex	16#01	Door status
TST Door and Control_1	0	1	68...91	3 %B70	Hex	16#00	Operation mode
Digital I/O_1	0	2	1	4 %D71	DEZ	400	Cycle counter
	0	3		5 %W75	Bin	2#0000_0001_0000_0010	Status bits
				6 %D77	DEZ+/-	-1	Maintenance counter
				7 %B81	Bin	2#0000_0000	Inputs 1/6
				8 %B82	Bin	2#0000_0000	Inputs 2/6
				9 %B83	Bin	2#0000_0000	Inputs 3/6
				10 %B84	Bin	2#0000_0000	Inputs 4/6
				11 %B85	Bin	2#0000_0000	Inputs 5/6
				12 %B86	Bin	2#0000_0000	Inputs 6/6
				13 %B87	Bin	2#1010_0110	Outputs 1/5
				14 %B88	Bin	2#0000_0000	Outputs 2/5
				15 %B89	Bin	2#0000_0000	Outputs 3/5
				16 %B90	Bin	2#0001_0000	Outputs 4/5
				17 %B91	Bin	2#0000_0000	Outputs 5/5